

FIG. 1

FIG. 1 of 17

2/17

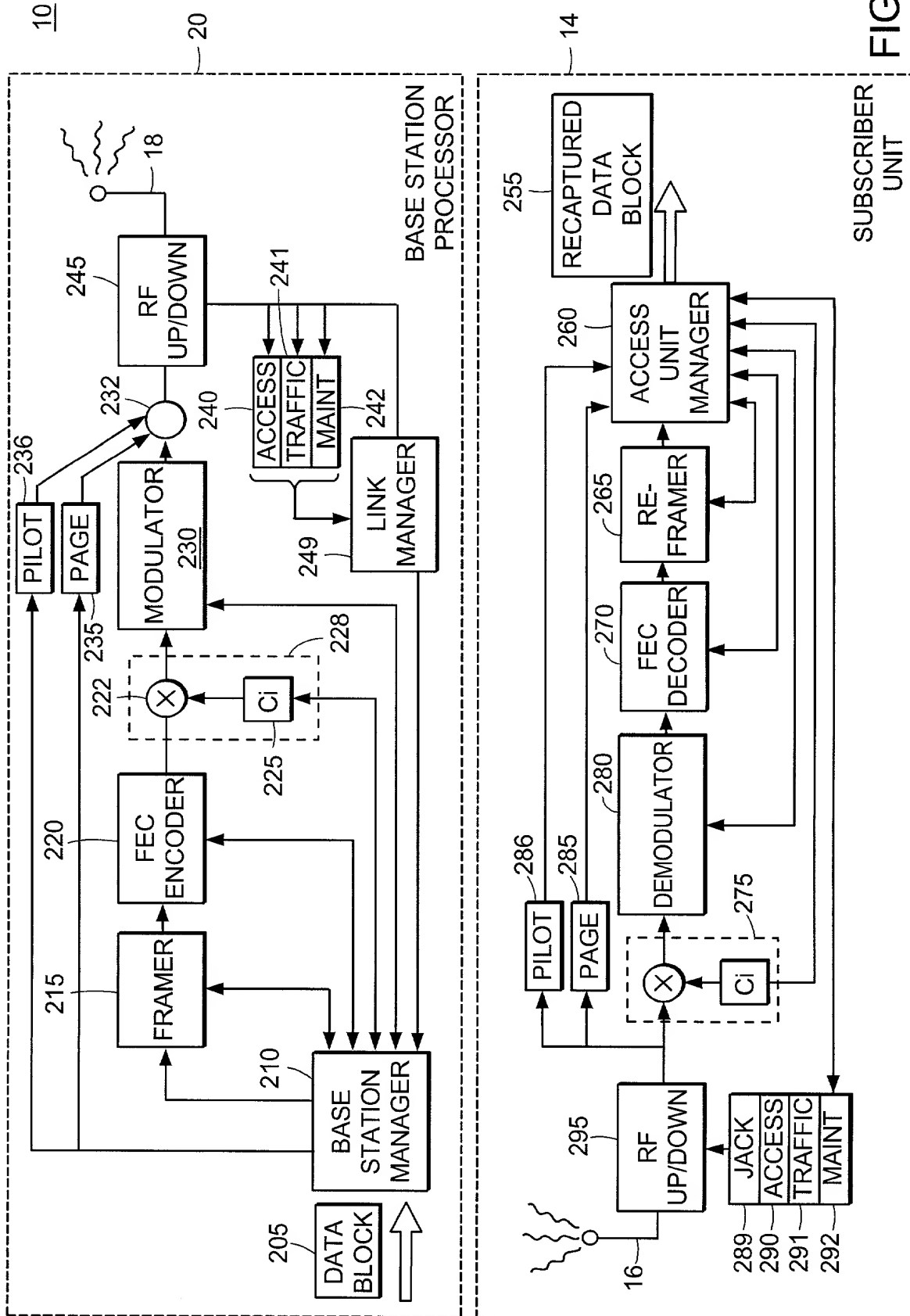


FIG. 2



FIG. 3

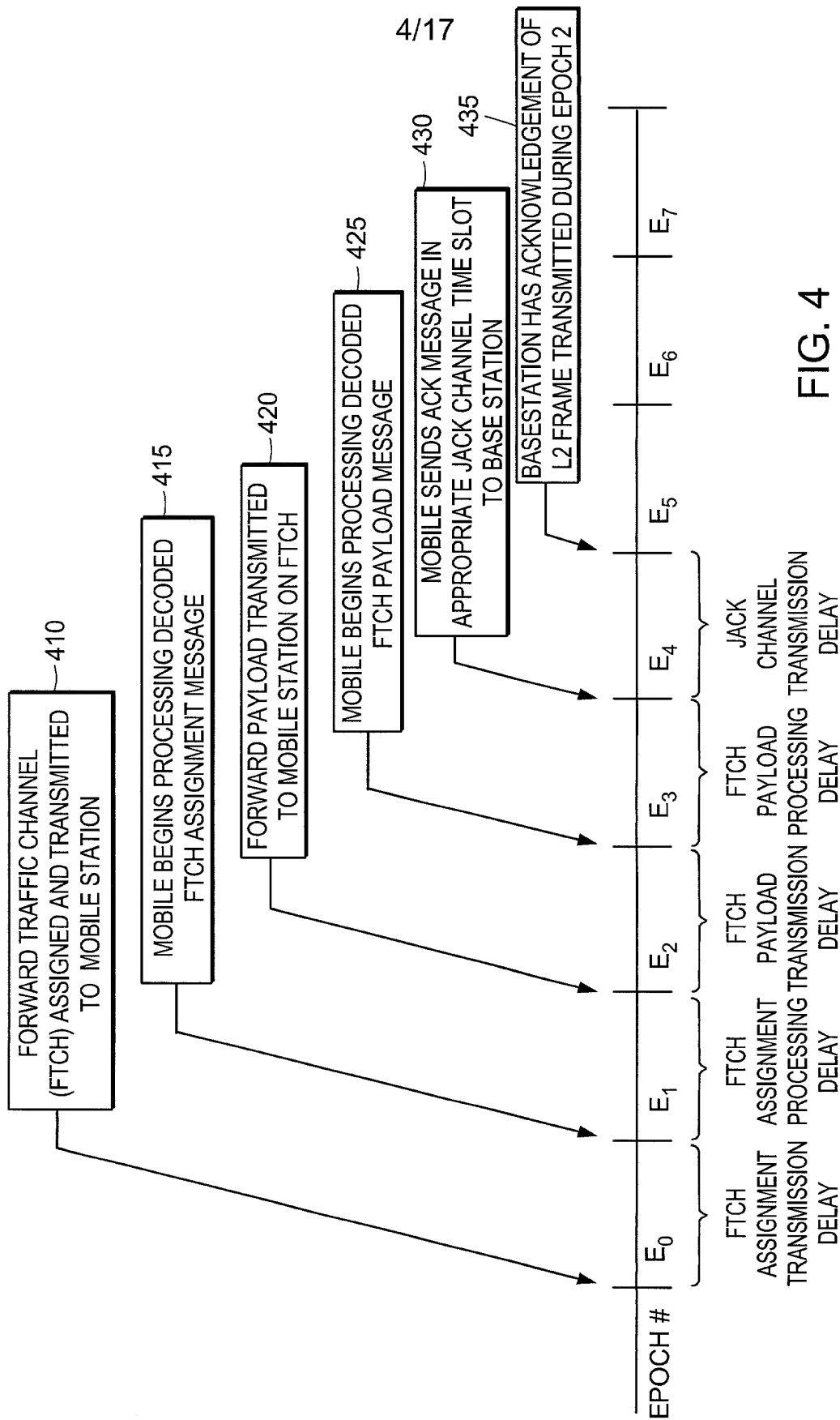


FIG. 4

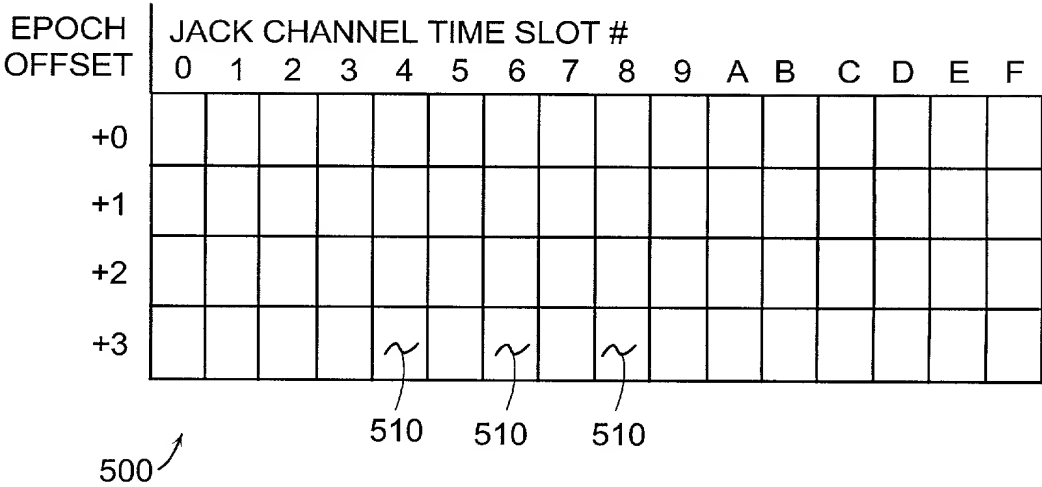


FIG. 5

344

LAYER 4 STREAM ID	32-BIT SOURCE ADDRESS	32-BIT DESTINATION ADDRESS	16-BIT SOURCE PORT	16-BIT DESTINATION PORT
0001	~~~~~	~~~~~	~~~~~	~~~~~
0002	~~~~~	~~~~~	~~~~~	~~~~~
0003	~~~~~	~~~~~	~~~~~	~~~~~
0004	~~~~~	~~~~~	~~~~~	~~~~~

FIG. 6

6/17

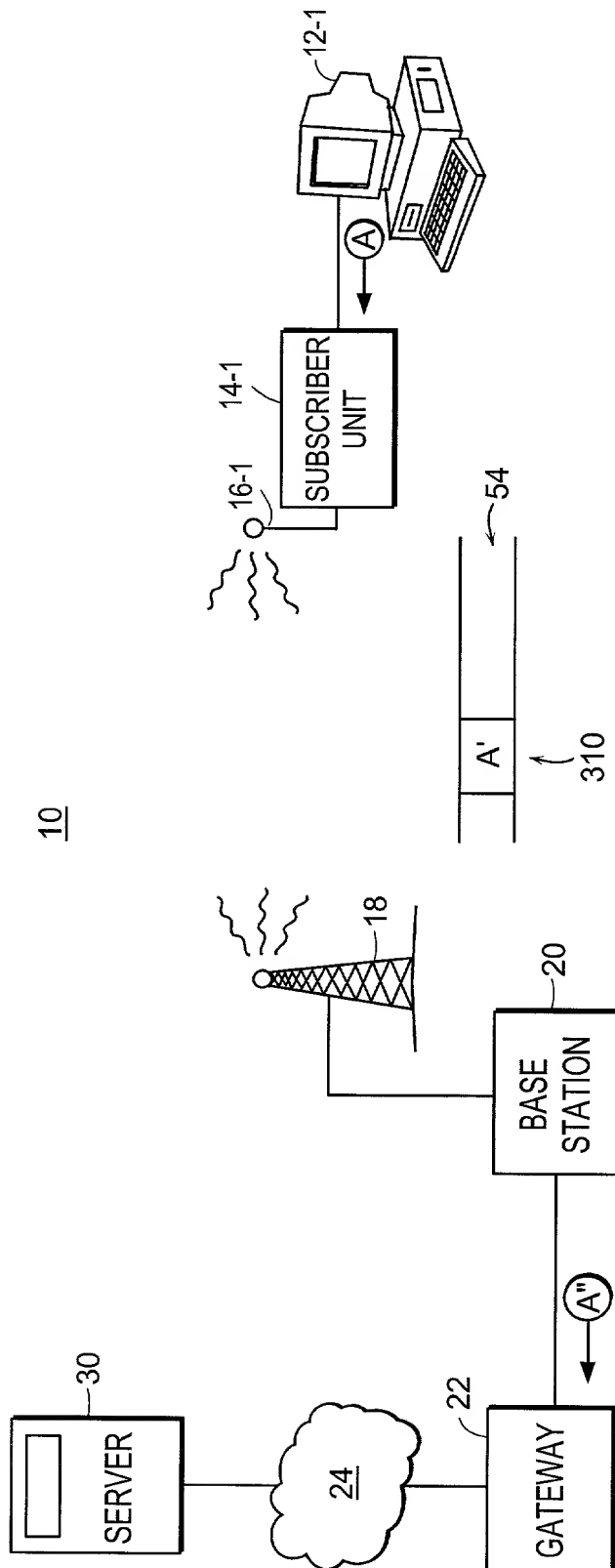


FIG. 7

7/17

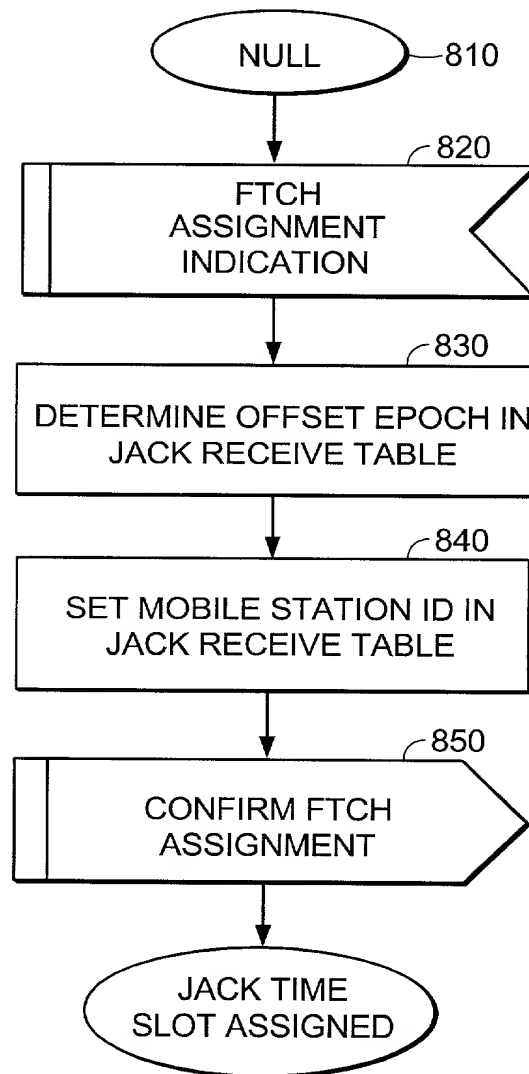


FIG. 8

8/17

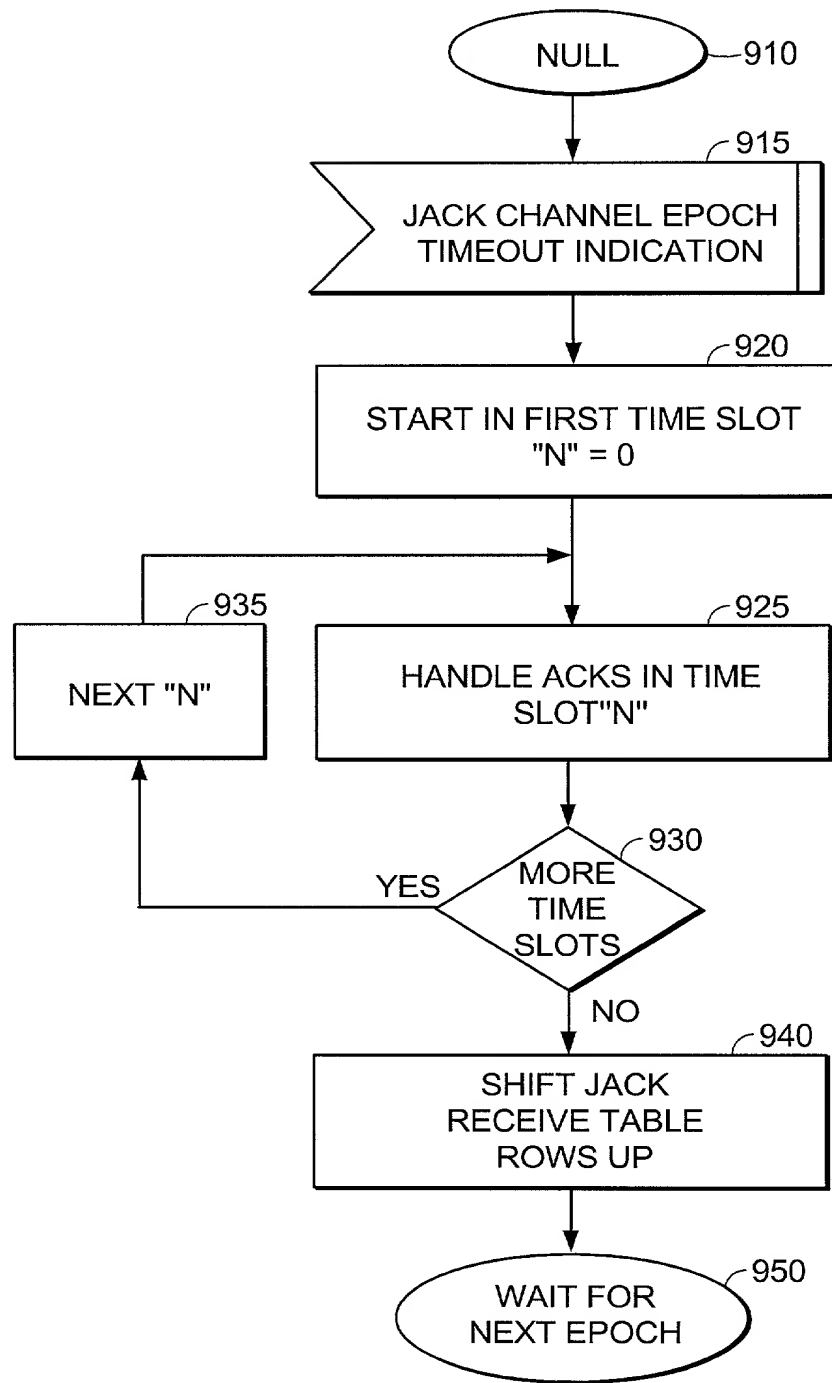


FIG. 9

9/17

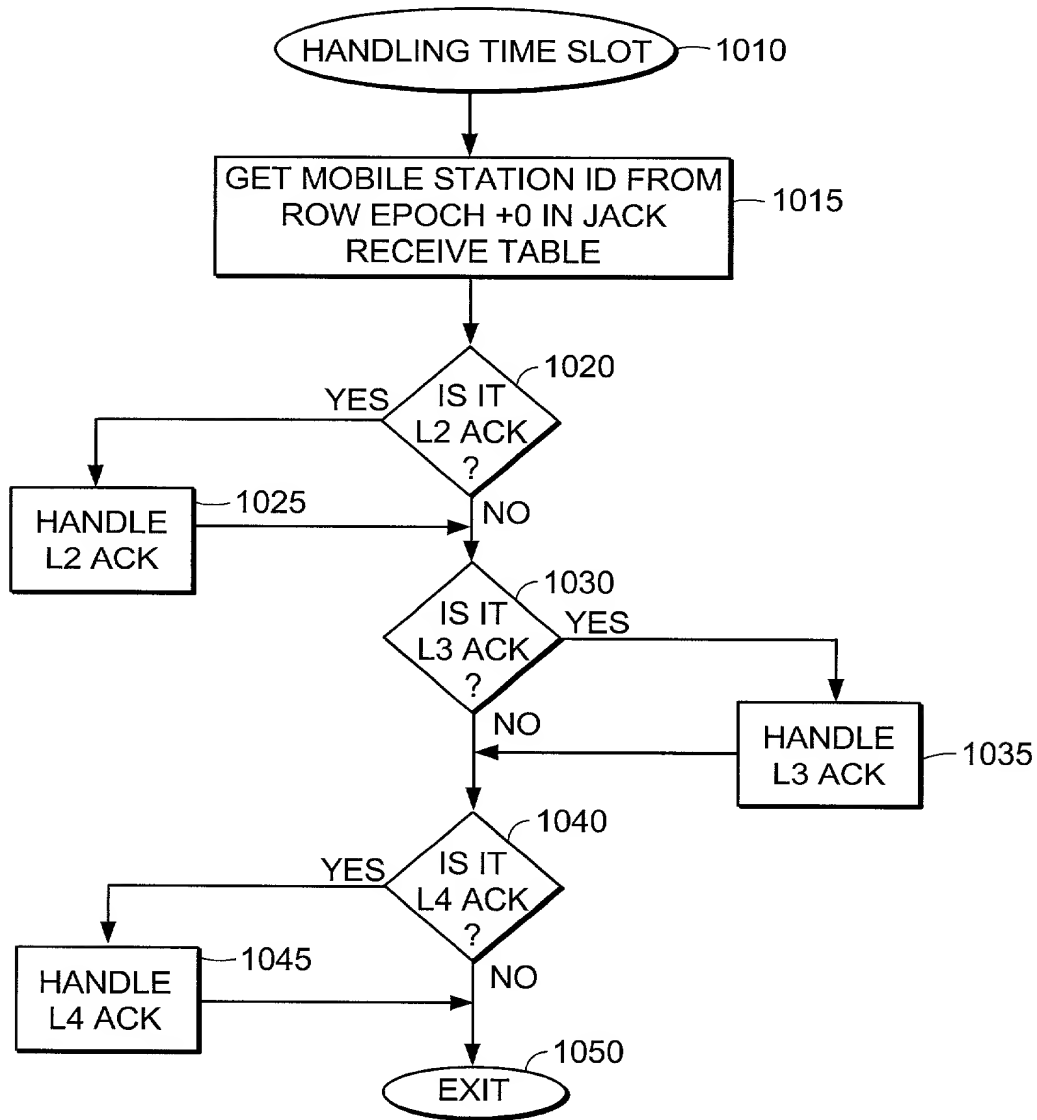


FIG. 10

10/17

FIG. 11

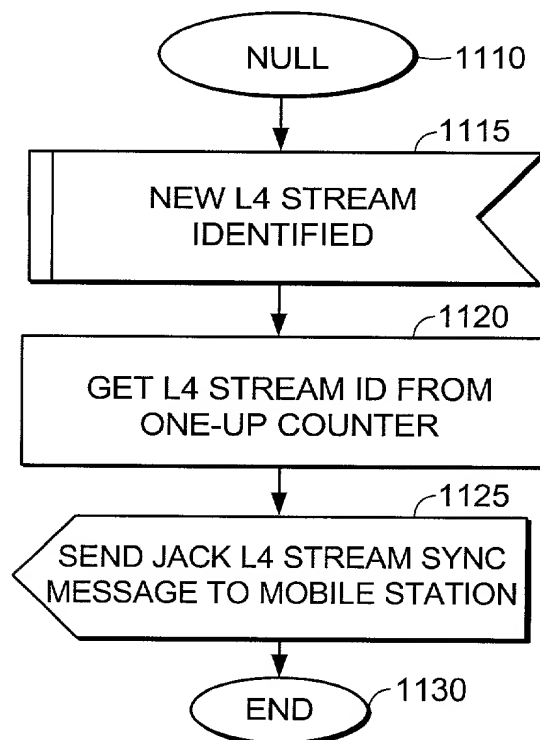
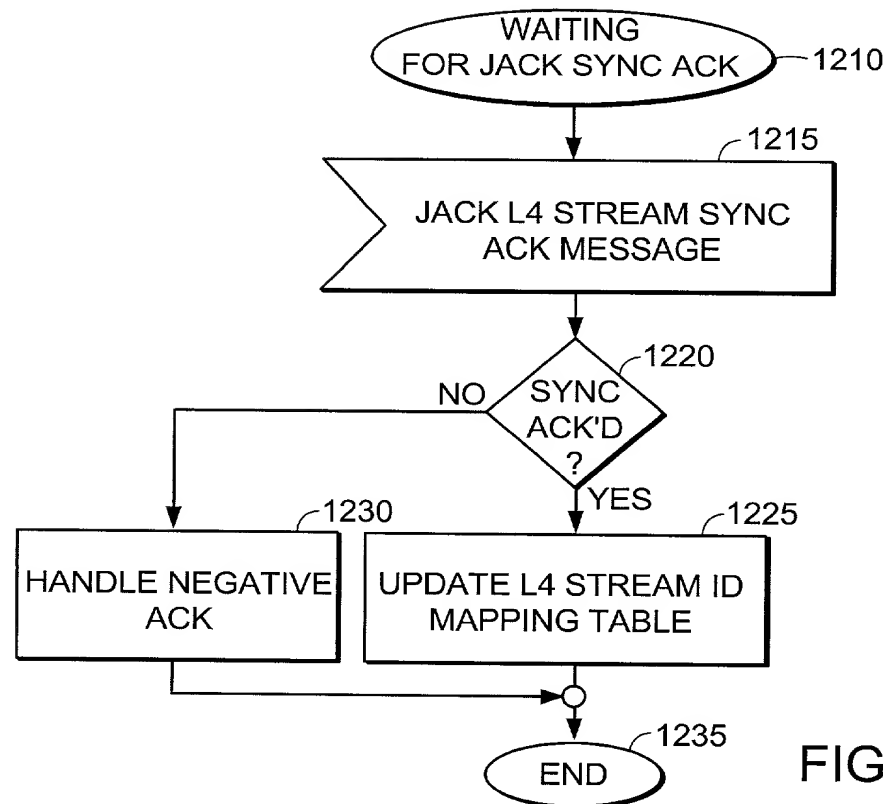


FIG. 12



11/17

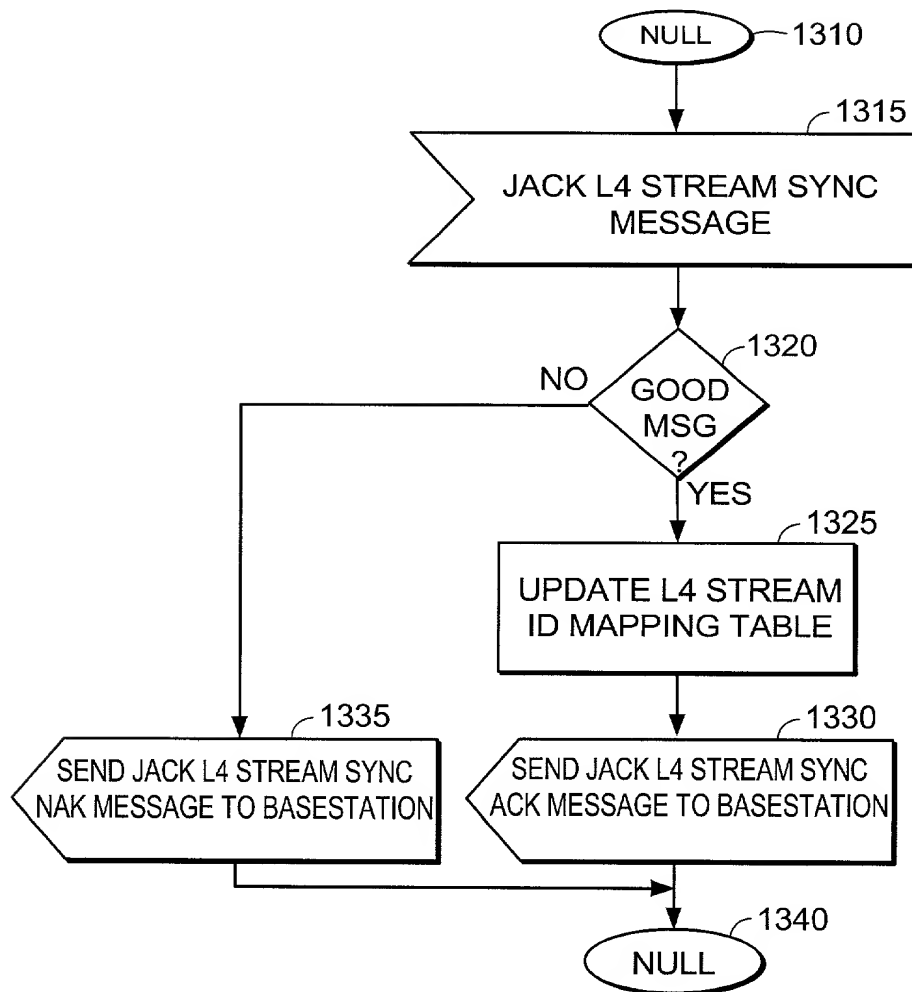


FIG. 13

12/17

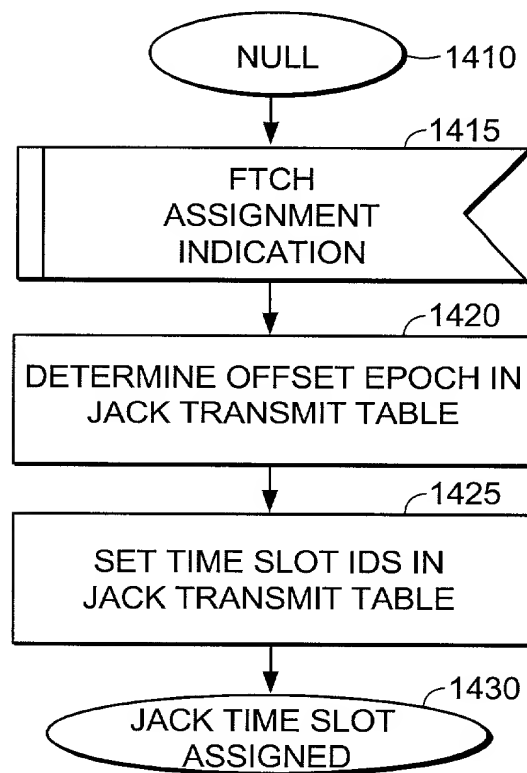


FIG. 14

13/17

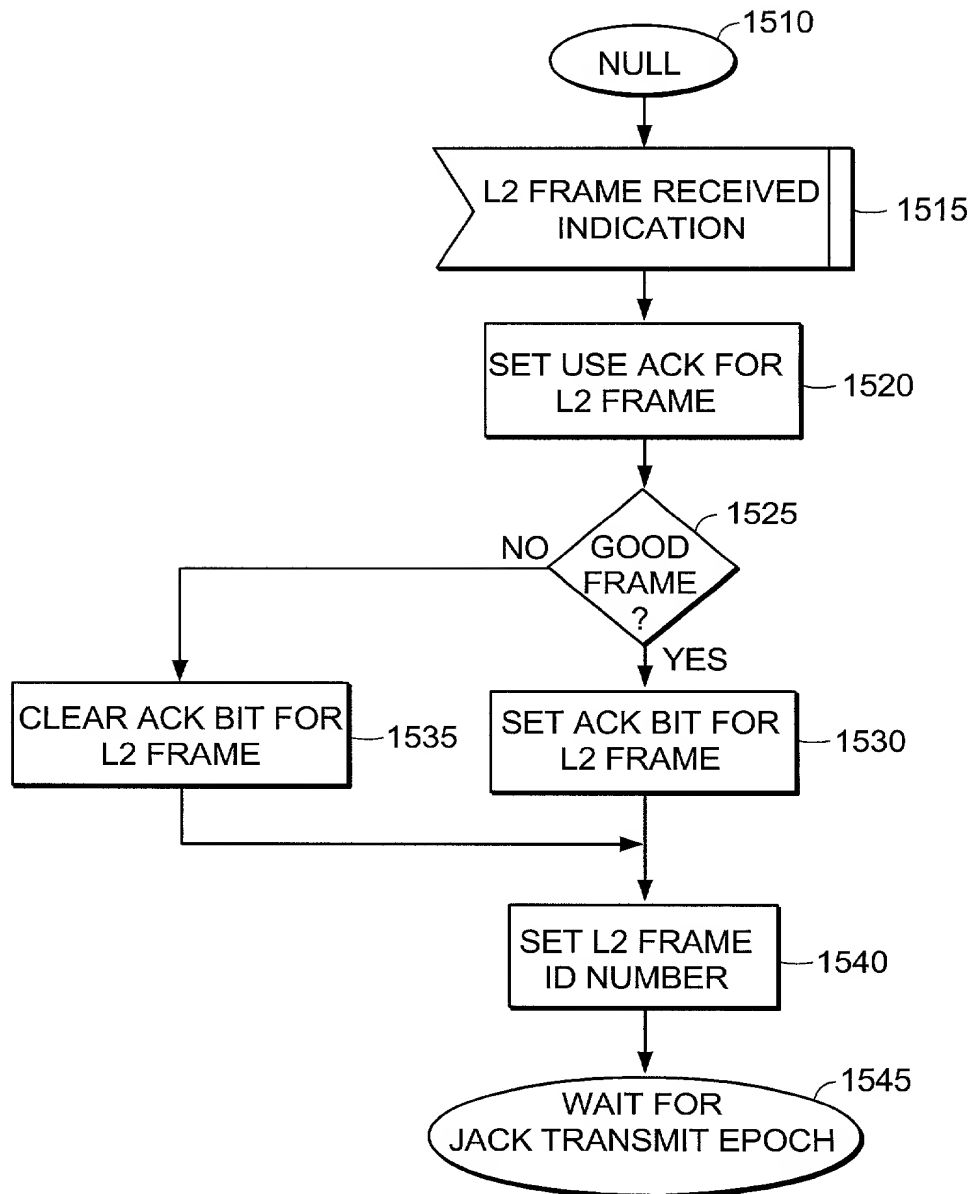


FIG. 15

14/17

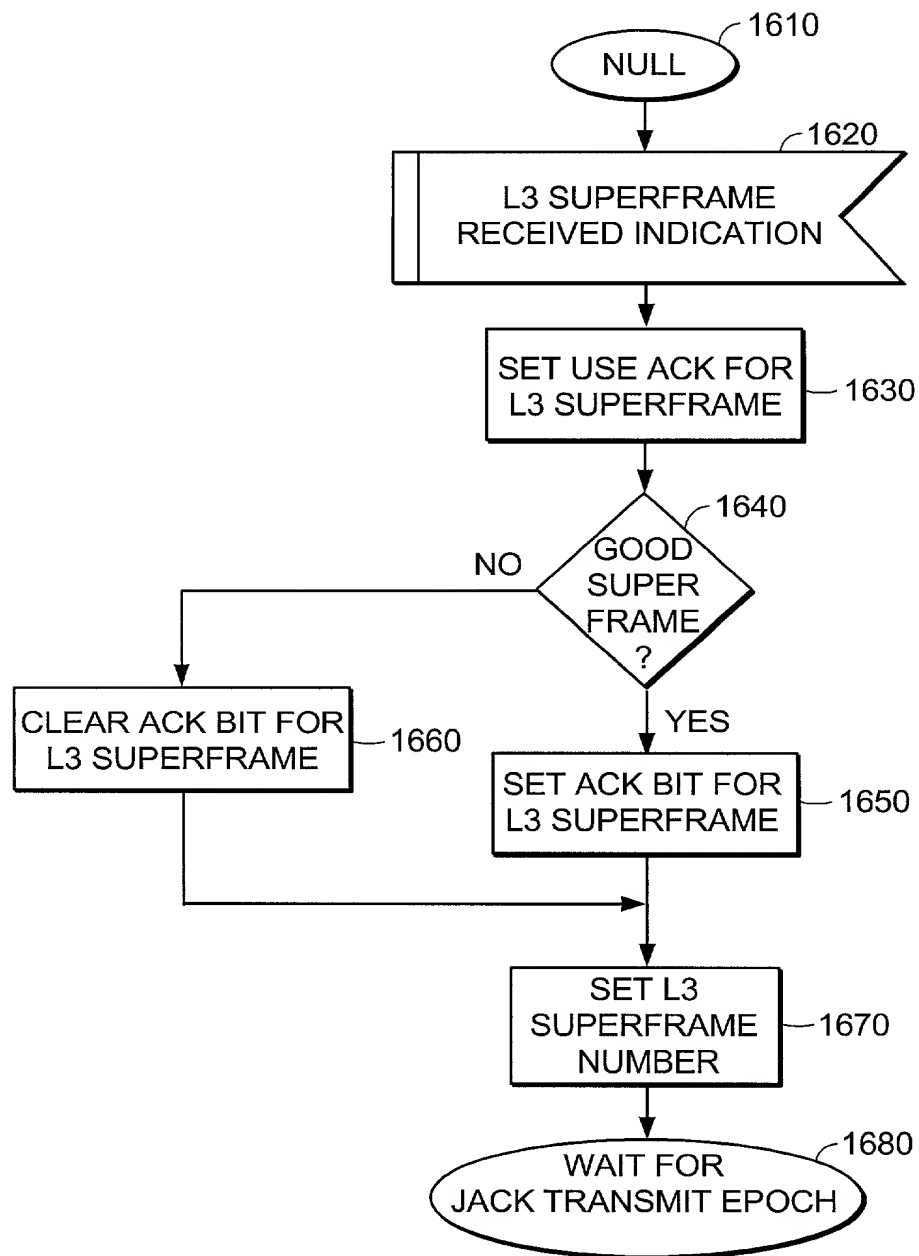


FIG. 16

15/17

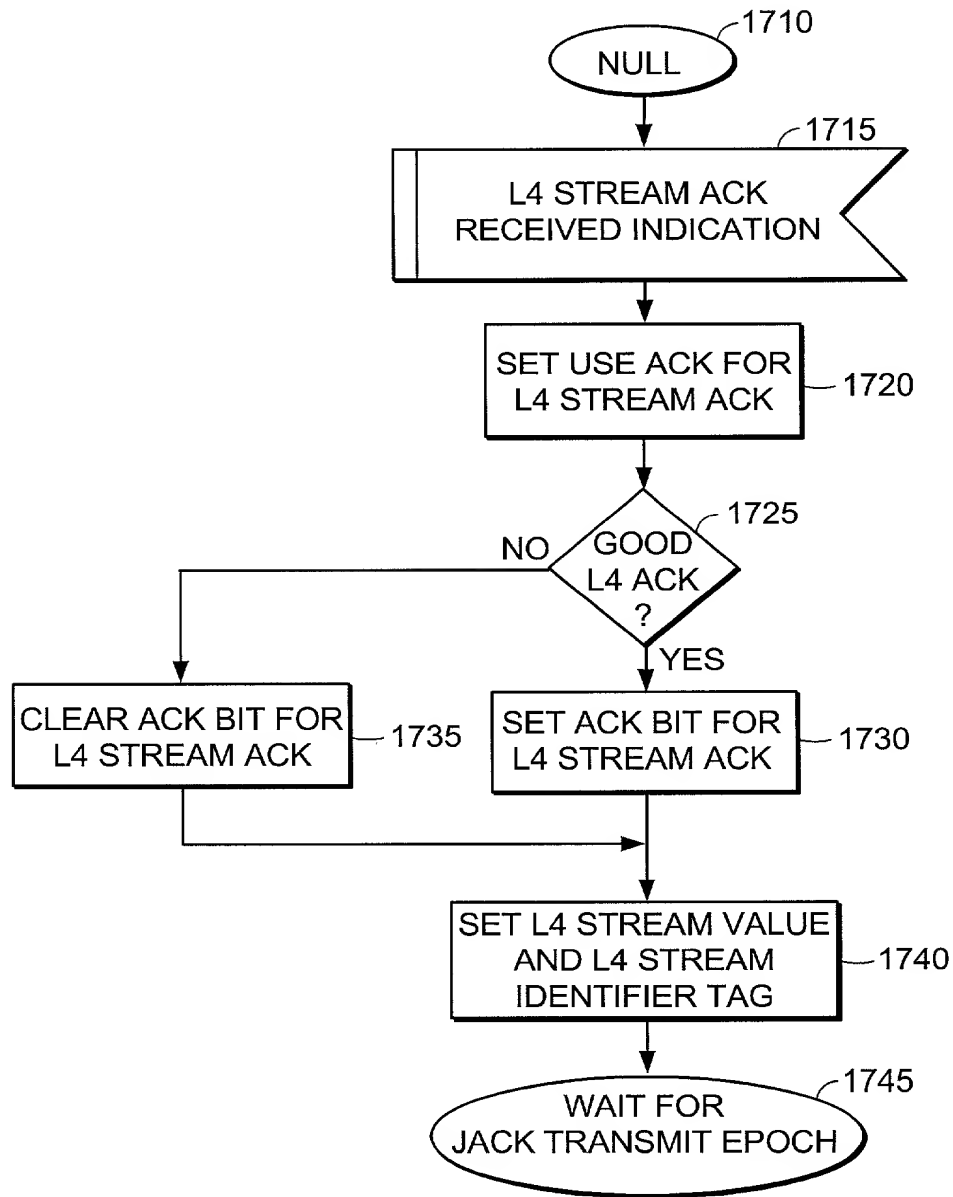


FIG. 17

16/17

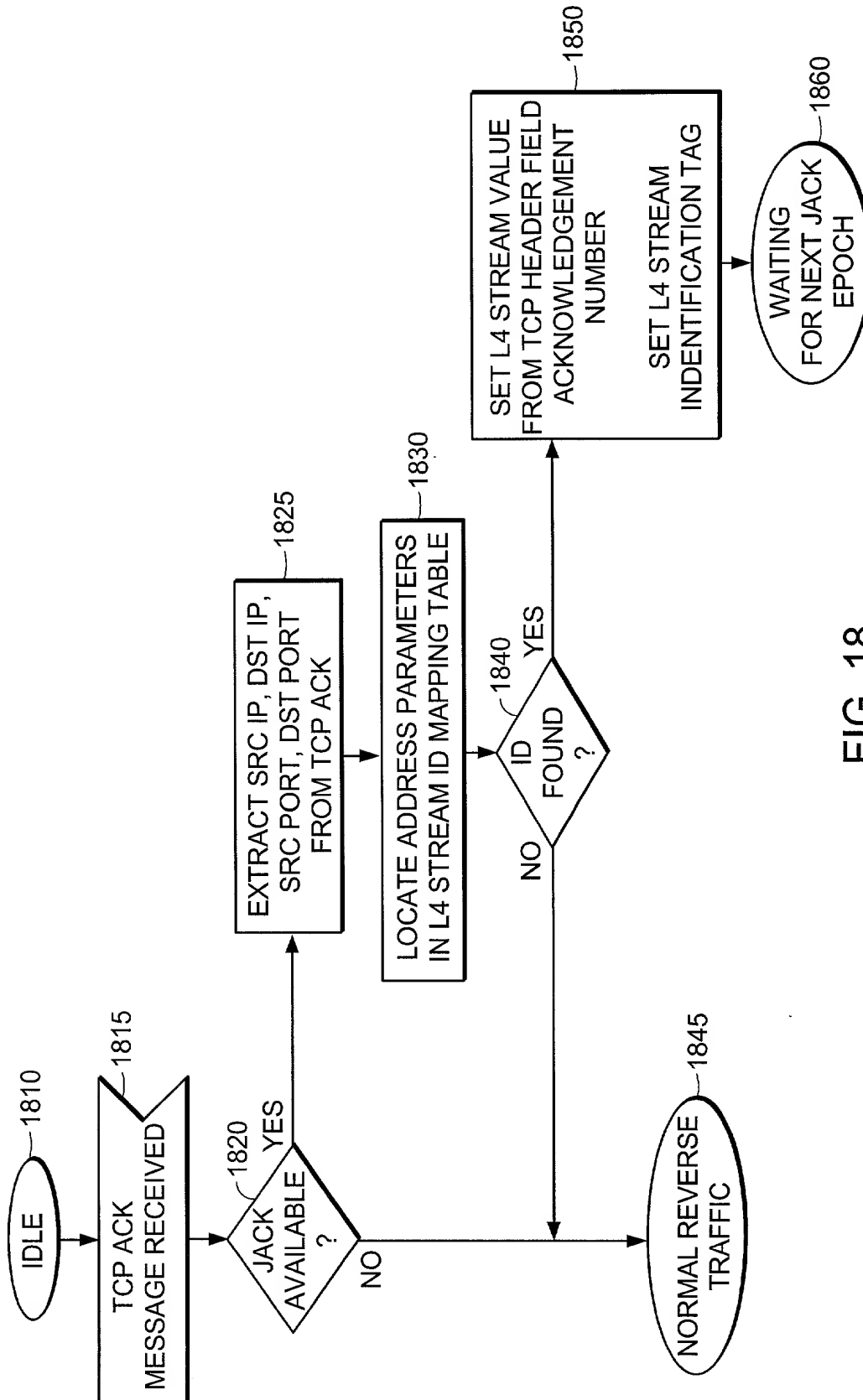


FIG. 18

17/17

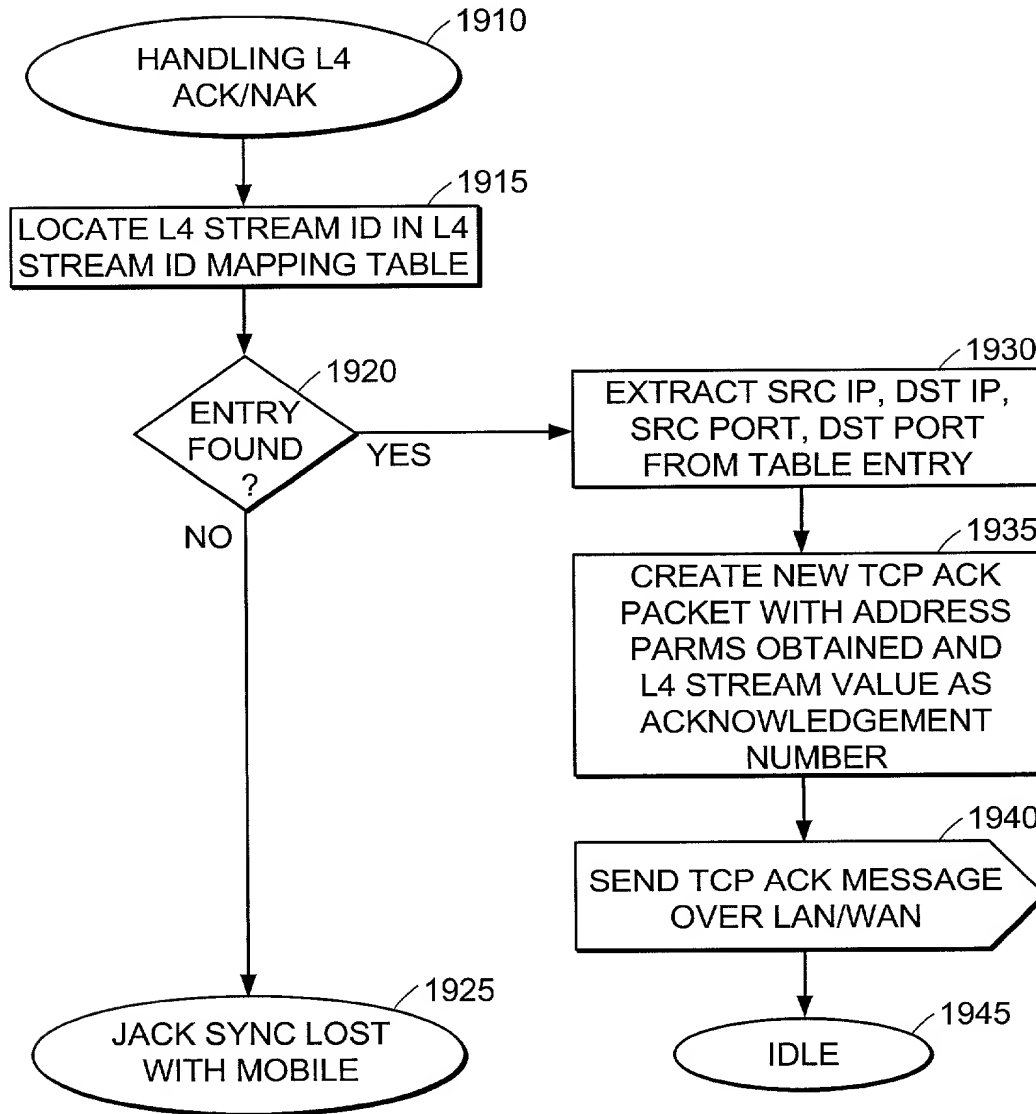


FIG. 19